ABSTRACT

An object of the invention is to provide a image display device in which the component cost and the equipment cost are reduced and a voltage level of a common electrode is easily adjustable to an optimum level. An image display device comprising a plurality of gate buses (G), a plurality of source buses (S), transistors (TFT) each of which is set to an on-state or an off-state in response to a voltage from a respective one of said gate buses (G) and supplies a voltage from said source bus (S) to a pixel electrode (2a) in said on-state, a common electrode (2c), and a corrected voltage supplying means for supplying a common electrode voltage (Vcom') which has been corrected by a predetermined amount of correction (ΔVcom) to said common electrode (2c), wherein said corrected voltage supplying means generates a first changing voltage for setting said transistor to said on-state and a second changing voltage for setting said transistor to said off-state to operate so as to establish a first supply mode, a second supply mode and a third supply mode, said first supply mode in which said first changing voltage is supplied to a predetermined number of ones of said plurality of gate buses and said second changing voltage is supplied to remaining ones of said plurality of gate buses, said second supply mode in which said first changing voltage is supplied to a larger number of ones of said plurality of gate buses than said predetermined number of gate buses and said second changing voltage is supplied to remaining ones of said plurality of gate buses, and said third supply mode in which said first changing voltage is supplied to a smaller number of ones of said plurality of gate buses than said predetermined number of gate buses and said second changing voltage is supplied to remaining ones of said plurality of gate buses; and determines the corrected common electrode voltage (Vcom'). (See Fig. 1)